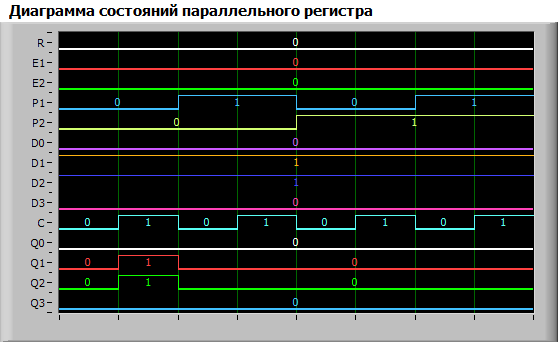
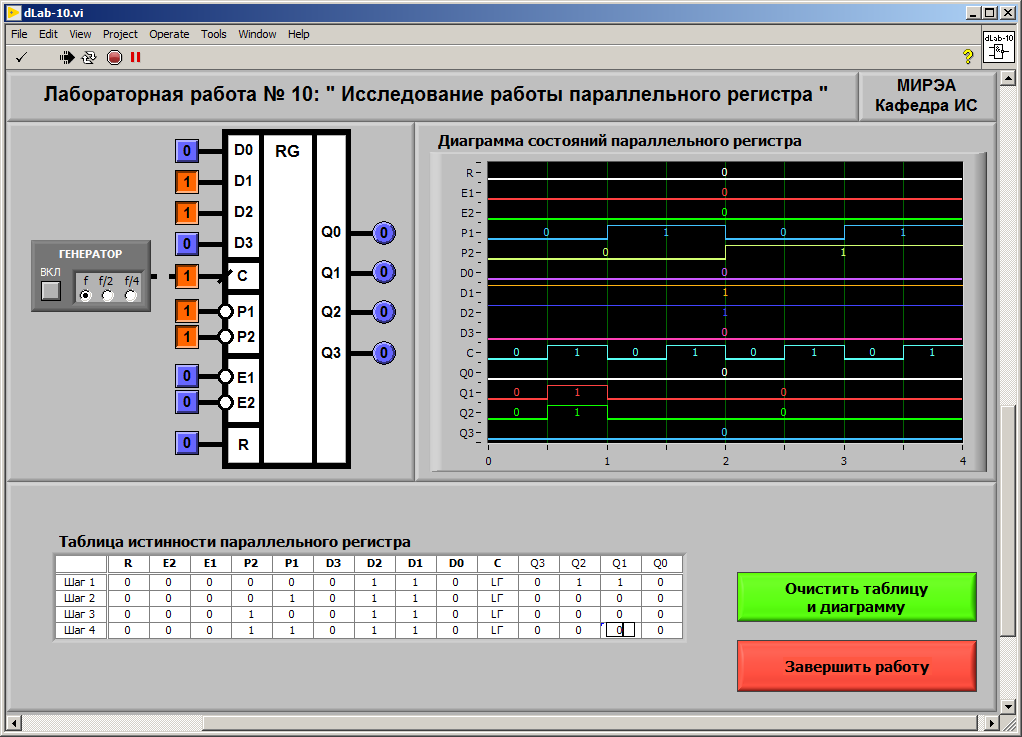
Paralel register

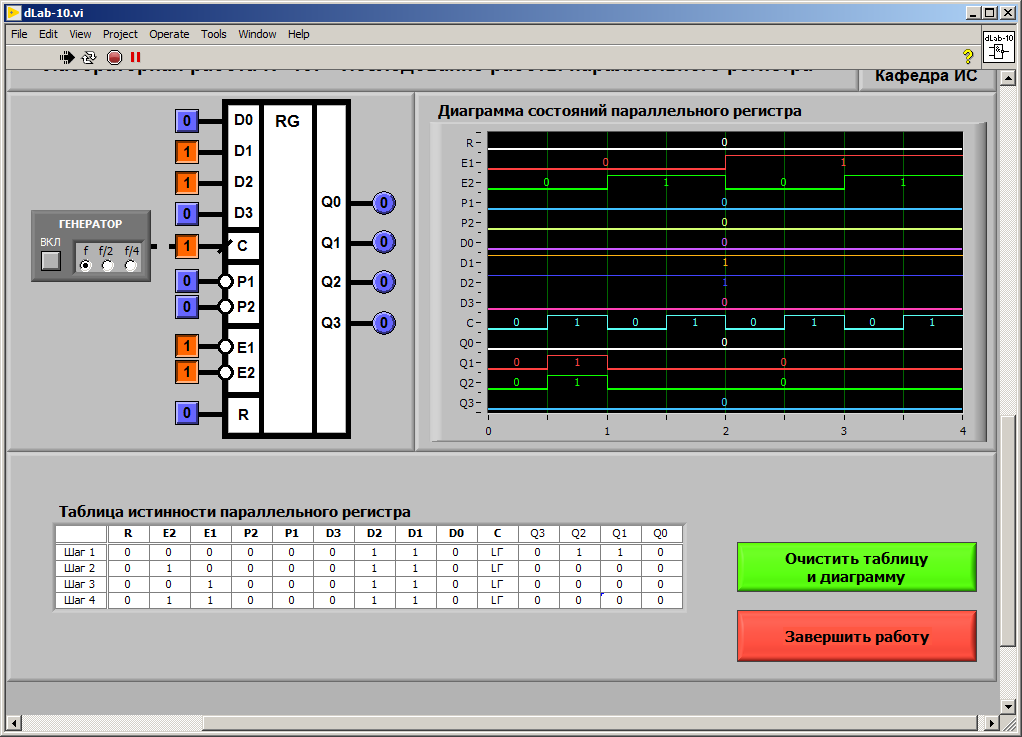




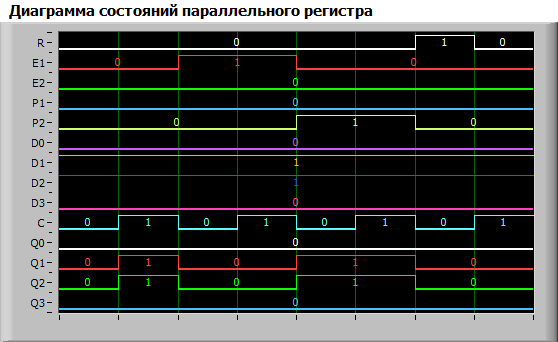


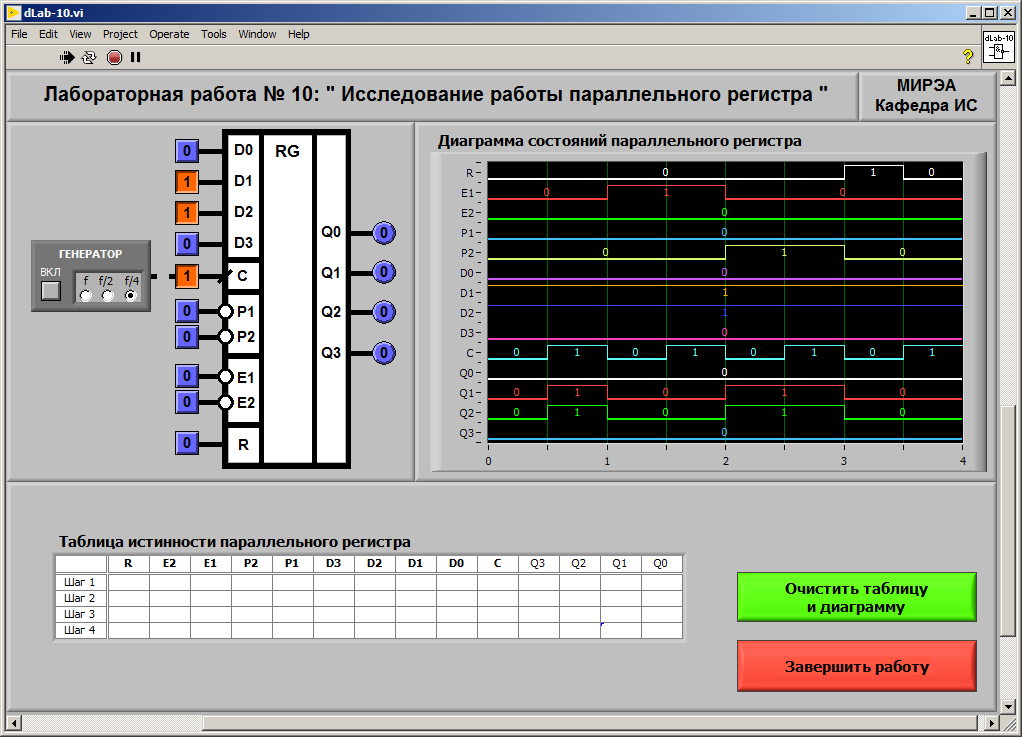






Dynamic

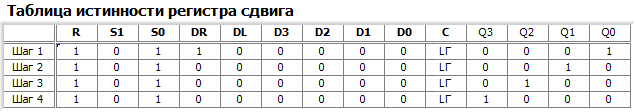


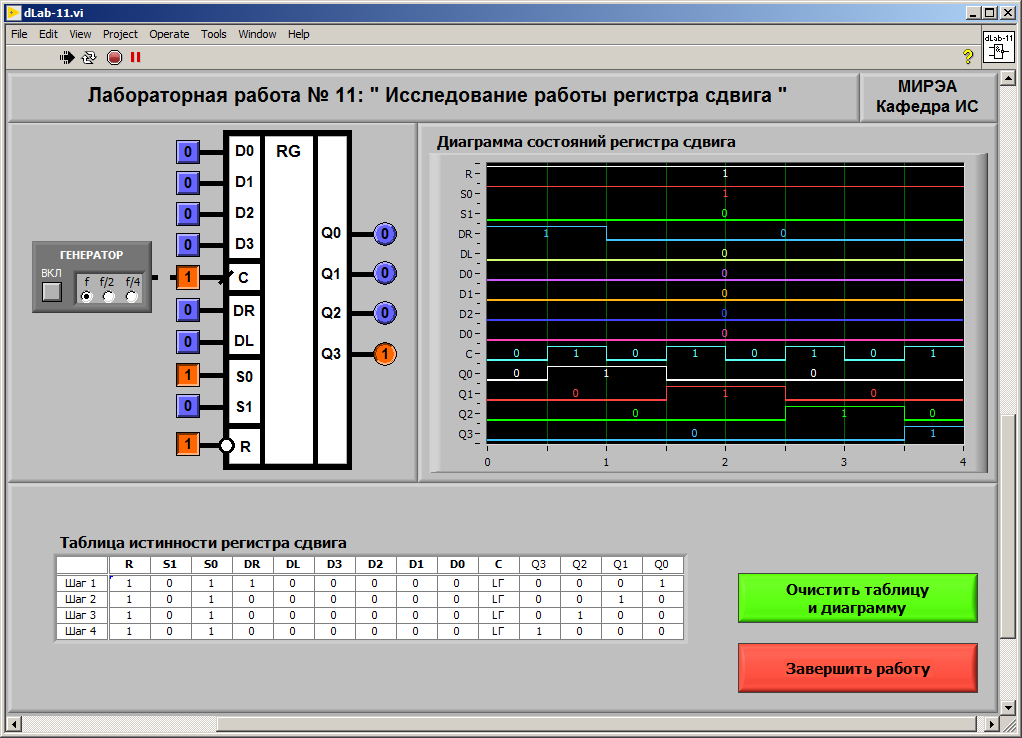


Shift register

Static

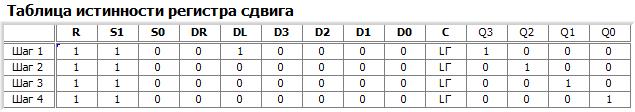


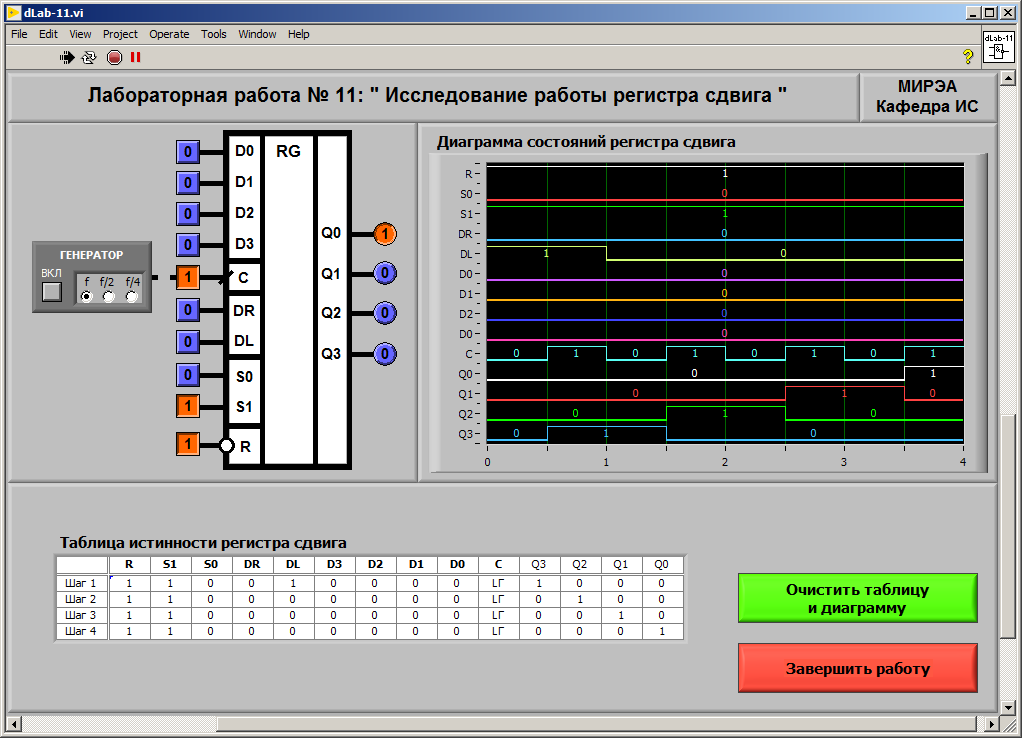
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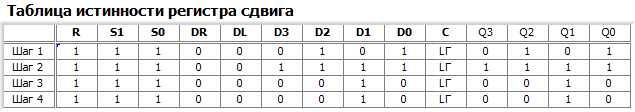
**Left**

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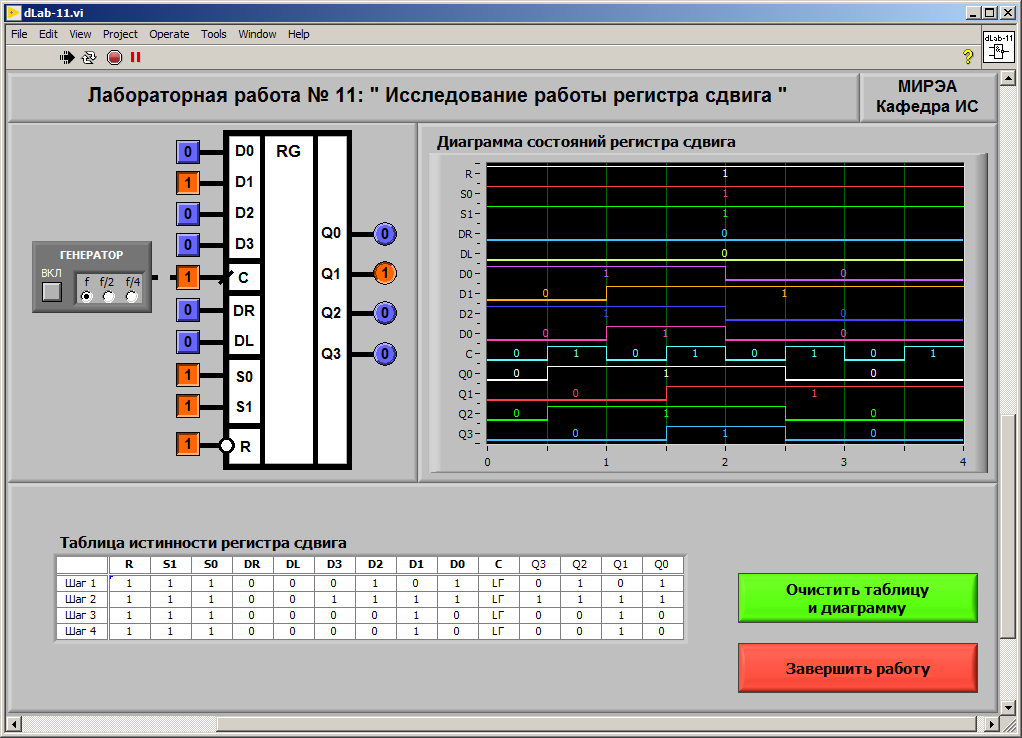




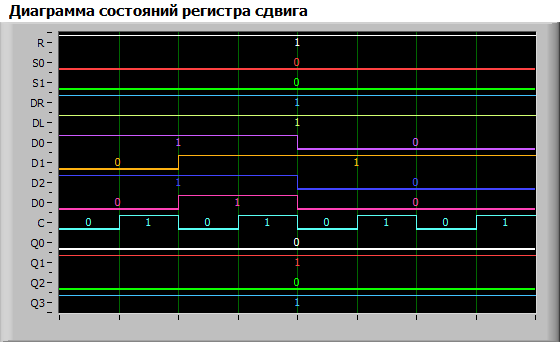
Parallel load

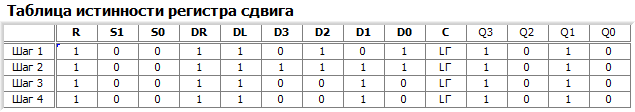


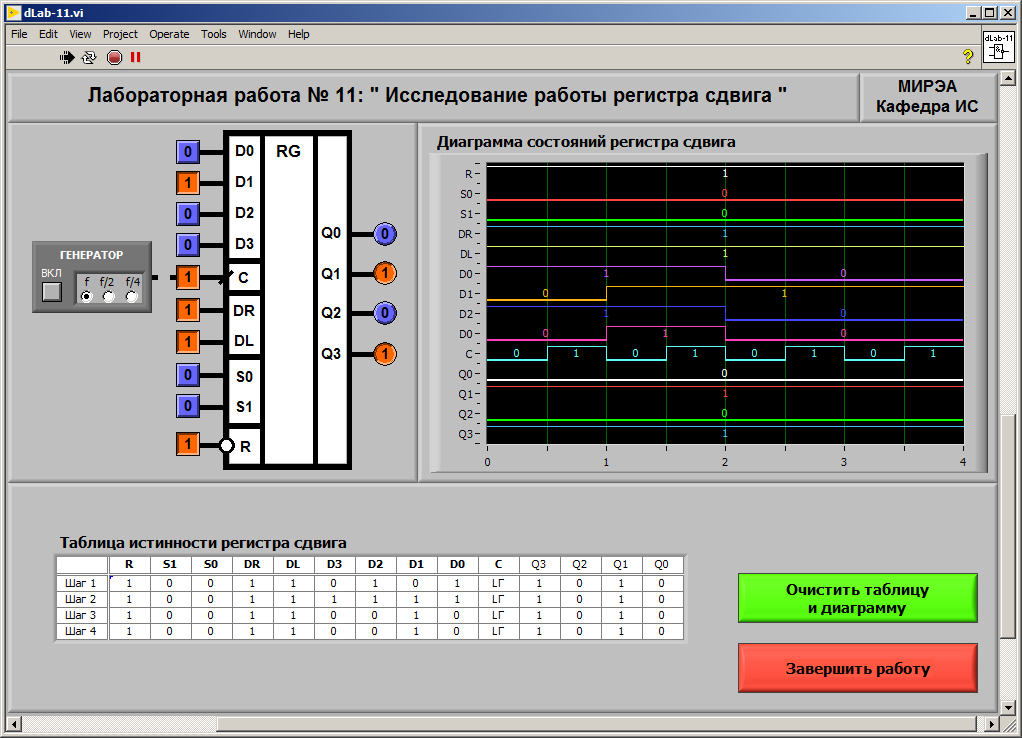




Storing







Dynamic



